

**A NOVEL CHARGE LEAKAGE CORRECTION CIRCUIT  
FOR APPLICATIONS IN PLLS**

**ABSTRACT**

5        An apparatus, a method, and a computer program are provided for correcting charge in a Phased Lock Loop (PLL). Typically, PLL's utilize a Low Pass Filter (LPF). However, as a result of improvement of Complimentary Metal-Oxide on a Semiconductor (CMOS) technology charge leakage has become 10 prevalent within LPFs. As a result, the method, apparatus, and computer program provide a device and/or methodology for correcting for charge leakage.